

BIHAR ENGINEERING UNIVERSITY, PATNA

Programme for M.Tech 2nd Semester Examination 2025

Time of Examination: 02:00 PM to 05:00 PM

Exam Id:- 250402

Patna, Date:- 02-03-2026

Day & Date	Paper Code	Paper Name
Monday, 16 Mar 2026	121201	VIBRATION ANALYSIS & DIAGNOSTICS
	122201	HEAT TRANSFER - II : CONVECTION HEAT TRANSFER
	123201	Analysis and Design of substructure
	131201	Research Methodology & IPR
	131204	Research Methodology And IPR
	141205	INTRODUCTION TO OPTO-ELECTRONICS DEVICES AND CIRCUIT
	171201	Low Power VLSI Design
	172201	Research Methodology & IPR
	173201	Research Methodology & IPR
	174201	Research Methodology & IPR
	175201	Research Methodology & IPR
	176201	Research Methodology & IPR
	177201	Research Methodology & IPR
	178201	Research Methodology & IPR
	179201	Research Methodology & IPR
	181201	Research Methodology & IPR
	182201	Research Methodology & IPR
	183201	Research Methodology & IPR
	184201	Research Methodology & IPR
	185201	Pavement Materials
186201	Low Power VLSI Design	
Wednesday, 18 Mar 2026	121202	NON-TRADITIONAL PARAMETER IN DESIGN
	122203	ENERGY CONSERVATION AND MANAGEMENT
	123202	Soil Dynamics and Machine foundation
	131201	Advanced Power System Protection
	131203	Advanced Power System Protection
	141201	ADVANCED SIGNAL PROCESSING & ITS APPLICATION
	171202	Analog IC Design
	172202	5G Technology
	173202	Advanced Algorithms
	174202	Wireless Communication
	175202	Structural Dynamics
	176202	Foundations of Theoretical Computer Science
	177202	Advanced Algorithms
	178202	ENERGY CONSERVATION AND MANAGEMENT
	179202	Digital Image Processing
	181203	Advanced Power System Protection
	182202	Casting and Welding Technology
183202	Cogeneration and Waste Heat Recovery	
184202	Casting and Welding Technology	
185202	Highway Construction Practices and Maintenance	
186202	Analog IC Design	
Thursday, 19 Mar 2026	123006	Hazardous Waste Management

Friday, 20 Mar 2026	121204	BEARING AND LUBRICATION
	122202	COMPUTATIONAL FLUID DYNAMICS
	123203	Rock Mechanics and Tunnel Engineering
	131202	Distributed generation and microgrid
	131202	Distributed generation and microgrid
	141202	ANALOG VLSI CIRCUITS
	171210	Soft Computing
	171211	Industrial Automation Control
	172203	Advanced Wireless Communication
	173203	Soft Computing
	173204	Data encryption & Compression
	174206	Advanced Optical Communication
	175203	Finite Element Analysis
	176203	Data Structure and algorithms
	177203	Soft Computing
	178203	Power System Optimization
	179203	Advanced Optical Communication
	181202	Distributed generation and microgrid
	182203	Robotics and Automation
	183203	Solar Energy
184203	Robotics and Automation	
185203	Advance Highway Geometrics and Design	
186213	Industrial Automation and Control.	
Monday, 23 Mar 2026	121205	ADVANCED COMPOSITES
	122204	ADVANCED TURBO-MACHINERY
	123204	Geosynthetics-II
	131205	High Voltage Engineering
	131211	Restructured Power Systems
	141203	DIGITAL LOGIC DESIGN
	171205	System on Chip
	172208	IOT and its Applications
	174203	Design of Microwave Passive Circuits
	175209	Rehabilitation and Retrofitting of Structures
	176219	Machine Learning
	177204	Data encryption & Compression
	178206	FACTS and its Application
	179207	Internet of Things
	181204	HVDC
	181205	High Voltage Engineering
	182210	Advanced Engineering Materials
	183207	Nuclear Power Plants
	184205	Flexible Manufacturing Systems
	185206	Airport Planning and Design
186207	System on Chip	
Wednesday, 25 Mar 2026	122600	APPLIED FINITE ELEMENT ANALYSIS
	123209	Mechanics of Sediment transport
	131210	Restructured Power Systems
	131211	Smart Grid
	131213	Energy Audit & Management
141204	DIGITAL CMOS IC DESIGN	

Wednesday, 25 Mar 2026	171203	CAD For VLSI Circuit
	172209	Optical Communication & Networks
	173208	Block chain Architecture and Applications
	176210	Compiler Design
	177208	Block chain Architecture and Applications
	178211	Restructured Power System
	181209	Electric and Hybrid Vehicles
	186203	CAD For VLSI Circuit

Note: Sessional/Practical/Viva-Voce Exam will be held from 11-03-2026 to 13-03-2026 and IA & External Marks should be uploaded during the same period.

By Order of the Vice-Chancellor
Sd/
Examination Controller
Bihar Engineering University
Patna

Memo No.:- BEU /Exam/ **383** /2026/Patna

Dated **02/03/26**..

Copy forwarded to (i) Secretary, Department of Science, Technology & Technical Education, Govt. of Bihar (ii) Director, Department of Science, Technology & Technical Education, Govt. of Bihar. (iii) The Principal/Director of Concerned College (iv) All District Magistrates (v) All Superintendents of Police for information and necessary action.


Examination Controller
Bihar Engineering University

Patna
02/03/26